

REMARKS

Claims 1-20 have been cancelled herein, and new claims 21-40 have been added. The new claims are directed towards the individual determination of data transfer rates with respect to each memory module in a plurality of memory modules. The claims are further directed towards the collective setting of a data transfer rate for the plurality of memory modules based at least in part upon the individually determined data transfer rates.

In contrast, the reference previously cited with respect to now cancelled claims 1-20 (Pollard, *et al.* (US 7,050,959)) teaches dynamic thermal management of a single integrated circuit, and does not disclose, teach, or suggest utilizing data transfer rate setting values with respect to two or more memory modules to collectively set a data transfer rate with respect to the two or more modules.

Therefore, reconsideration and allowance of the claims is respectfully requested.

Respectfully submitted,

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MJM:cg